

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

:

Masaki YAMADA et al

: GROUP ART UNIT: 2826

SERIAL NO. 10/003,114

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FILED: December 6, 2001

: EXAMINER: P. GREENE

FOR: SEMICONDUCTOR DEVICE

HAVING BURIED CONDUCTIVE

LAYER AND METHOD OF MANUFACTURING THEREOF



ELECTION

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Restriction Requirement dated July 16, 2003, Applicants elect Group 1, Claims 1-10, drawn to a semiconductor device, classified in class 257, subclass 249.

Accordingly, examination on the merits of Claims 1-10 is believed to be in order, and an early and favorable action to that effect is respectfully requested.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,

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COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/003,114

Applicants: Masaki YAMADA, et al.

Filing Date: December 6, 2001

For: SEMICONDUCTOR DEVICE HAVING BURIED CONDUCTIVE LAYER AND METHOD OF

MANUFACTURING THEREOF

Group Art Unit: 2826 Examiner: P. GREENE

SIR:

Attached hereto for filing are the following papers:

ELECTION

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAJER & NEUSTADT, P.C.

Eckhard H. Kuesters

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